

31911 U.S. PRO
10/010994

Je. 1137



U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10010994	FILING DATE 11/12/2001	CLASS 707	SUBCLASS 711	GAU 2476	EXAMINER M.K.M.
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****APPLICANTS:** Osborne Randy; 154 2185

****CONTINUING DATA VERIFIED:**

**** FOREIGN APPLICATIONS VERIFIED:**

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials		ATTORNEY DOCKET NO 004667.P12472
TITLE : Method and apparatus for read launch optimizations in memory interconnect <small>U.S. DEPT. OF COMM. / PAT. & TM-PTO-426L (Rev. 12-94)</small>		

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NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner	
		Application Examiner	
PREPARED FOR ISSUE			
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